

[2885/84]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Martin Vorbach et al.
Serial No.: To be Assigned
Filed: Herewith
For: I/O AND MEMORY BUS SYSTEM FOR DFPS AND UNITS
WITH TWO- OR MULTI-DIMENSIONAL
PROGRAMMABLE CELL ARCHITECTURES

Examiner: To be Assigned
Art Unit: To be Assigned

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

S I R:

Kindly amend the above-identified application before examination, as set forth below.

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 3 of this paper.